ESP8266 Serial Esp-01 WIFI Wireless Transceiver Module Send Receive LWIP AP+STA

Feature:

SDIO 2.0, SPI, UART

32-PIN QFN Package

Integrated RF Switch, balun, 24dbm PA, DCXO, and PMU

Integrated RISC processer, on-chip memory and external memory interfaces

Integrated MAC/basband processors

I2S interface for high fidelity audio applications

On-chip low-dropout linear regulators for all internal supplies

Proprietary spurious free clock generation architecture

Integrated WEP, TKIP, AES ,and WAPI engines

Supports APSD for optimal VOIP applications

Patented spurious noise cancellation algorithm for integration in SOC applications

Supports Bluetooth co-existence interface

Self-calibrated RF to ensure optimal performance under all operating conditions

802.11b/g/n

Wi-fi direct, soft-AP

Integrated TCP/IP protocol stack

Integrated TR switch, balun, LNA, power amplifier and matching network

Integrated PLLs, regulators, DCXO and power management unites

+19.5dBm output power in 802.11b mode

Power down leakage current of <10uA

Integrated low power 32-bit CPU could be used as application processor

SDIO 1.1/2.0, SPI, UART

STBC, 1\*1MIMO, 2\*1MIMO

A-MPDU & A-MSDU aggregation &0.4ms guard interval

Wake up and transmit packets in<2ms

Standby power consumption of<1.0mW

Package list:

1 x ESP8266 Module